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#2	(((packet <or> p2p <or> point-to-point) <and> (bus? <or> bus) <and> ((coherency <or> snoop* <or> directory) <near/2> cache))<in>metadata)	7
#3	(((detect* <or> determin*) <near/2> (cache <near/2> (coherency <or> snoop* <or> directory)) <and> (point-to-point <or> p2p <or> bus <or> buss <or> packet*))<in>metadata)	0
#4	(((detect* <or> determin*) <near/2> (cache <near/2> (coherency <or> snoop* <or> directory)) <and> (point-to-point <or> p2p <or> bus <or> buss <or> packet*))<in>metadata)	0
#5	((detect* <or> determin*) <near/2> (cache <near/2> (coherency <or> snoop* <or> directory))<IN>metadata)	0
#6	(((vbi <or> (virtual <near/2> (bus <or> buss <or> interface))) <and> (cache <or> coheren*) <and> (packet* <or> point-to-point <or> p2p))<in>metadata)	4
#7	(((vbi <or> (virtual <near/2> (bus <or> buss <or> interface))) <and> (cache <or> coheren*) <and> (packet* <or> point-to-point <or> p2p))<in>metadata)	4
#8	(((vbi <or> (virtual <near/2> (bus <or> buss <or> interface))) <and> (cache <or> coheren*) <and> (packet* <or> point-to-point <or> p2p))<in>metadata)	4

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1. Performance of pruning-cache directories for large-scale multiprocessors

Scott, S.L.; Goodman, J.R.;
Parallel and Distributed Systems, IEEE Transactions on
Volume 4, Issue 5, May 1993 Page(s):520 - 534
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2. Fast and cost effective cache invalidation in DSM

Chang-Kyu Lee; Jong Hyuk Choi; Kyu Ho Park; Bong Wan Kim;
Parallel and Distributed Systems, 2000. Proceedings. Seventh International Conference on
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3. The MINC (Multistage Interconnection Network with Cache control mechanism) chip

Midorikawa, T.; Kamei, T.; Hanawa, T.; Amano, H.;
Design Automation Conference 1998. Proceedings of the ASP-DAC '98. Asia and South Pacific
10-13 Feb. 1998 Page(s):337 - 338
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4. SCILab-a simulation environment for the Scalable Coherent Interface

Bin Wu; Bogaerts, A.;
Modeling, Analysis, and Simulation of Computer and Telecommunication Systems, 1995. MASCOTS '95.,
Proceedings of the Third International Workshop on
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5. XDBus: a high-performance, consistent, packet-switched VLSI bus

Sindhu, P.; Frailong, J.-M.; Gastinel, J.; Cekleov, M.; Yuan, L.; Gunning, B.; Curry, D.;
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6. A cache coherency scheme for an asynchronous packet-switched shared memory multiprocessor

Alles, S.; Mahmud, S.;
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3. Photonic multihop bus networks

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INFOCOM '91. Proceedings. Tenth Annual Joint Conference of the IEEE Computer and Communications Societies.
Networking in the 90s. IEEE
7-11 April 1991 Page(s):981 - 990 vol.3

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4. Data broadcasting-the ultimate push technology?

Richardson, D.;
Broadcasting Convention, 1997. International
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